

FACULTY DEVELOPMENT PROGRAMME (FDP) on "IOT ENABLED PLC"

30 OCTOBER TO 4 NOVEMBER 2023

## Aim & Objective

The aim and objective of this ATAL-FDPs is to make future engineers acquainted with conceptual as well as practical knowledge of industrial automation and control systems. It will also focus on the significance and application of automation and control technologies in manufacturing industries. Moreover, it will focus on the latest developments like Artificial Intelligence, Machine Learning, IoT etc. which are changing role of automation and control.



#### B K BIRLA INSTITUTE OF ENGINEERING & TECHNOLOGY PILANI



#### ORGANIZED BY DEPARTMENT OF ELECTRICAL ENGINEERING

## **About BKBIET**

B K Birla Institute of Engineering & Technology, Pilani is an institute of international repute, was founded by Shree Krishnapan Charity Trust as its technical wing under the Chairmanship of Late Syt. Basant Kumar Birla in the year 2007. The institute is approved by AICTE & is affiliated with Bikaner Technical University, Bikaner for master's degree programs in Computer Science Engineering and VLSI Design and Technology and undergraduate programmes in Computer Science Engineering (NBA Accredited), Information Technology, Electronics & Communication Engineering, VLSI Design and Technology, Electrical Engineering, Computer Science Engineering (Data Science) and Artificial Intelligence. The institute has dedicated faculties, modern infrastructure, state of art laboratories & workshops, libraries with a rich collection of books, journals, a training & research centre, and good medical facilities. The institute has facilities of wellfurnished boy's and girl's hostels, gym, playgrounds, and courts for different Sports. The institute is sprawled over 18 acres of land located in the vicinity of campuses Birla Technical Training Institute, Central Electronics Engineering Research Institute, and Birla Institute of Technology and Science, Pilani. BKBIET, Pilani provides the highest quality technical education and first-rate environment to engineering students from all over India to pursue their academic, intellectual, moral, and cultural skills.

### **About EE Department**

To emerge as a front-runner for catering latest needs of Power Sector, Electrical Industries, Public Service and to improve the quality of life with high human values. Imparting quality education and skills by establishing the state-of-the-art research facilities to students that further contributes to the socio-economic growth and eventually contributing to sustainable development of the society. Electrical Engineering course provides students with an understanding in the area of Electrical Power Generation, Transmission and Distribution, Power System Analysis, Machine Design, Power Electronics, Control System, Renewable Energy Technologies (Focus on Islanded/Grid connected Solar PV System and Wind Power Generation), Smart Grid, Microgrid, Energy Storage, Fuzzy Logic, Neural Network, Artificial Intelligence and various simulation tools. Professional Training and Research Centre of the Institute has advanced Control Panels, Machine Control, Bearing Technologies, Machine design and latest PLC-SCADA Lab.

#### Chief Patron(s)

Dr. Prasanna Kumar S. M., Director, BKBIET, Pilani Mr. K. K. Pareek, G.M. Commercial, BKBIET, Pilani

#### Patron

Dr. Anil Kumar Sharma, Principal, BKBIET, Pilani

#### **Organising Committee**

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#### **Coordinator Details**

Mr. Rajesh Singh Shekhawat

**Co-Coordinator Details** Mr. Anuj Sharma

#### **Student Coordinators**

Sachin Chinmay Jain Nitin Jangir Kapil Kumar Pushpendra Sharma

## **RESOURCE PERSON**



**Dr. Suchandan Pal** Chief Scientist & Group Head CSIR-CEERI, Pilani



**Dr. Dheerendra Singh** Professor BITS Pilani



**Dr. H O Bansal** Professor, BITS, Pilani



**Dr. Sumitra Singh** Principal Scientist CSIR-CEERI Pilani



**Dr. S M Prasanna Kumar** Director BKBIET, Pilani



**Dr. C B Gupta** Professor North Cap University, Gurugram



**Mr. Vivek Vishwakarma** Engineer PLC Trainer



**Dr. Sk. Masiul Islam** Senior Scientist CSIR-CEERI Pilani



**Dr. Vivek Kumar** Professor & Head EEE BRCM, Bahal



**Mr. Dinesh Saini** Engineer RVPNL Rajasthan

## SCHEDULE

Day 1	Day 2	Day 3	Day 4	Day 5	Day 6
30/10/23	31/10/23	1/11/23	2/11/23	3/11/23	4/11/23
9:00 – 9:30 Inauguration	-	-	-		-
9:30 – 12:00 Session 1	9:30 – 12:00 Session 3	9:30 – 12:00 Session 5	9:30 – 12:00 Session 7	9:30-1:00 Birla Science Museum Visit	9:30 – 12:00 Session 10
12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion		12:00 – 1:00 Reflection Journal
1:00 – 2:00 Lunch (Mess-1)	1:00 – 2:00 Lunch (Mess-1)	1:00 – 2:00 Lunch (Mess-1)	1:00 – 2:00 Lunch (Mess-1)	1:00 – 2:00 Lunch (Mess-1)	1:00 – 2:00 Lunch (Mess-1)
2:00 – 4:30 Session 2	2:00 – 4:30 Session 4	2:00 – 4:30 Session 6	2:00 – 4:30 Session 8	2:00 – 4:30 Session 9	2:00 – 4:00 MCQ, Feedback & Interactions
4:30 – 5:30 Practical sessions/Labs (BPTRC Lab)	4:30 – 5:30 Practical sessions/Labs (BPTRC Lab)	4:30 – 5:30 Practical sessions/Labs (BPTRC Lab)	4:30 – 5:30 Practical sessions/Labs (BPTRC Lab)	4:30 – 5:30 Practical sessions/Labs (BPTRC Lab)	4:00 – 5:00 Valedictory Session

### **Targeted Participants**

Associate Professor Assistant Professors Ph.D. Scholar's PG Students UG Students.

# How to Reach Pilani, Rajasthan

Pilani is about 200 km west of Delhi and about 220 km north of Jaipur. Direct buses to Pilani are frequently available from Delhi (ISBT Kashmiri Gate) and Jaipur (RSRTC bus stop, Sindhi Camp). Trains are available from Sarai Rohilla (Delhi) railway station to Loharu (25km from Pilani).

# **Contact Us**

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